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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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EXAMINER

TRUJILLO, JAMES K

ART UNIT PAPER NUMBER

2116

DATE MAILED: 10/12/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/809,106

Applicant(s)

YAMADA ET AL.

Examiner

James K. Trujillo

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 July 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,4,5,7,8,10,13,14 and 17 is/are rejected.
- 7) ☒ Claim(s) 2,3,6,9,11,12,15 and 16 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☒ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

1. It is hereby acknowledged that the following papers have been received and placed of record in the file: Amendment dated 7/28/05
2. Claims 1-17 are presented for examination.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1, 4-5, 7-8, 10, 13-14, and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dunstan et al., U.S. Patent 5,964,879 in view of Hetzler, U.S. Patent 5,954,820.
5. Regarding claim 1, Dunstan teaches an information device having means for supplying power to a plurality of its components including an OS and a driver (Operating system 88 wherein it is inherent that the OS has a driver in order to communicate with the rest of the system, figure 5) and a device driver (device drive 98), the information device (computer systems 10 and 50 figures 1 and 4) characterized by comprising:
 - a. means for monitoring components from predetermined single one or a number of said components, and for detecting peak-power generating condition and peak- power terminating condition for the monitored components (storage for a power characterization

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table for the devices, power coordinator and power budgeter, col. 4, lines 19-25, col. 6, lines 35-47);

- b. power-mode changing means for switching mode of power, predetermined single one or a number of said components, from a normal-power mode to a power saving mode (standby mode) according to detected information from said monitoring means on said peak-power generating condition (required power not available), and for switching the power mode from said power-saving mode to said normal-power mode according to detected information from said access monitoring means on said peak-power terminating condition (the power budgeter determines a peak-power condition “lack of available power” and switching one of the other components “other devices” into an off mode or a standby mode, col. 9, lines 30-41). Dunstan implicitly teaches wherein switching the power mode from said power-savings mode to said normal-power mode on said peak-power terminating condition (col. 2, lines 5-9, col. 8, line 64 through col. 9 line 9, figure 7). Specifically, Dunstan further teaches that peak-power conditions are temporary, such as starting up a disk. One of ordinary skill would understand that the components that are placed in a power-saving mode would be placed back in the normal-power mode when the peak-power generation condition is terminated in order to provide any functionality lost during the peak-power generation condition;
- c. wherein said monitoring means monitors between said OS and driver, and said device driver (power coordinator 90).

Dunstan does not disclose wherein the monitoring means *is an access monitoring means for monitoring IO packets* from said predetermined single one or a number of said components [emphasis added].

Hetzler teaches an access monitoring means for monitoring IO packets (accesses are interpreted include IO packets and detection of accesses inherently requires an access monitoring means, col. 2, line 63 through col. 3, line 11, col. 8 lines 1-29 and col. 8, line 52 through col. 9, line 19). The access monitoring means of Hetzler provide the advantage of improved performance because the user will not see the latency and improved energy savings because power modes can be entered with shorter delays (col. 8, lines 58-64).

It would have been obvious to one of ordinary skill in the art, having the teachings of Dunstan and Hetzler before them at the time the invention was made, to modify the monitoring means of Dunstan by using the access monitoring means as taught by Hetzler in order to obtain an access monitoring means to detect peak-power generating and terminating conditions (by including the access monitoring means or Hetzler within the power coordinator of Dunstan). In using the teaching of Hetzler in Dunstan, the power changing would be applied to components (such as an LCD display), which are not monitored. Specifically, Dunstan suggests wherein a display should have its backlight reduced rather than be turned off during a disk drive operation to improve performance (col. 2, lines 23-35 and col. 3, lines 52-58).

One of ordinary skill in the art would have been motivated to make the modification in order to achieve the advantages of improved performance and improved energy savings in view of the teachings of Hetzler.

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6. Regarding claim 4, Dunstan together with Hetzler taught the information device according to claim 1, as described above. Dunstan further teaches wherein at least one of said monitored components is a processor (controller inherently in disk subsystems 64 and 66) characterized in that the monitoring means is configured for detecting as the peak-power generating condition a rate surpassing a set value (power for requesting device is not available, col. 9, lines 30-41) and detecting as the peak-power terminating condition a rate dropping below a set value (implicitly, after power is available).

Hetzler teaches wherein the access monitoring means are configured for finding use rate (access frequencies) for the processor (wherein the processor is IDE controller 6, which controls data transfer for a disk drive, col. 4, lines 42-44 and col. 8, lines 30-64).

7. Regarding claim 5 and 7, Dunstan together with Hetzler taught the claimed information device. Therefore together they also teach the claimed method for using the information device.

8. Regarding claim 8, Dunstan together with Hetzler taught the claimed information device. Therefore together they also teach the claimed recording medium for storing the claimed program.

9. Regarding claim 10, Dunstan teaches a information device having means for supplying power to a plurality of its components, the information device characterized by comprising:

- a. means for monitoring components which are monitored among said plurality of components, and for detecting peak-power generating condition and peak- power terminating condition for the monitored components (storage for a power characterization table for the devices, power coordinator and power budgeter, col. 4, lines 19-25, col. 6, lines 35-47);

- b. power-mode changing means for switching mode of power, to the components among said plurality of components, from a normal-power mode to a power saving mode (standby mode) according to detected information from said monitoring means on said peak-power generating condition (required power not available), and for switching the power mode from said power-saving mode to said normal-power mode according to detected information from said access monitoring means on said peak-power terminating condition (the power budgeter determines a peak-power condition “lack of available power” and switching one of the other components “other devices” into an off mode or a standby mode, col. 9, lines 30-41). Dunstan implicitly teaches wherein switching the power mode from said power-savings mode to said normal-power mode on said peak-power terminating condition (col. 2, lines 5-9, col. 8, line 64 through col. 9 line 9, figure 7). Specifically, Dunstan further teaches that peak-power conditions are temporary, such a starting up a disk. One of ordinary skill would understand that the components that are placed in a power-saving mode would be placed back in the normal-power mode when the peak-power generation condition is terminated in order to provide any functionality lost during the peak-power generation condition.
- c. wherein said monitoring means monitors between said OS and driver, and said device driver (power coordinator 90).

Dunstan does not disclose wherein the monitoring means *is an access monitoring means* and wherein the components to which power changing is applied are *components which are not monitored* among said plurality of components [emphasis added].

Hetzler teaches an access monitoring means for monitoring information on access to components (detection of accesses inherently requires an access monitoring means, col. 2, line 63 through col. 3, line 11, col. 8 lines 1-29 and col. 8, line 52 through col. 9, line 19). Hetzler further teaches that some components (LCD display) cannot have their accesses monitored because of difficulty in such type of monitoring. The access monitoring means of Hetzler provide the advantage of improved performance because the user will not see the latency and improved energy savings (col. 8, lines 58-64).

It would have been obvious to one of ordinary skill in the art, having the teachings of Dunstan and Hetzler before them at the time the invention was made, to modify the monitoring means of Dunstan by using the access monitoring means as taught by Hetzler in order to obtain an access monitoring means to detect peak-power generating and terminating conditions (by including the access monitoring means of Hetzler within the power coordinator of Dunstan). In using the teaching of Hetzler in Dunstan, the power changing would be applied to components (such as an LCD display), which are not monitored. Specifically, Dunstan suggests wherein a display should have its backlight reduced rather than be turned off during a disk drive operation to improve performance (col. 2, lines 23-35 and col. 3, lines 52-58).

One of ordinary skill in the art would have been motivated to make the modification in order to achieve the advantages of improved performance and improved energy savings in view of the teachings of Hetzler.

10. Regarding claim 13, Dunstan together with Hetzler taught the information device according to claim 10, as described above. Dunstan further teaches wherein at least one of said

monitored components is a processor (controller inherently in disk subsystems 64 and 66) characterized in that the monitoring means is configured for detecting as the peak-power generating condition a rate surpassing a set value (power for requesting device is not available, col. 9, lines 30-41) and detecting as the peak-power terminating condition a rate dropping below a set value (implicitly, after power is available).

Hetzler teaches wherein the access monitoring means are configured for finding use rate (access frequencies) for the processor (wherein the processor is IDE controller 6, which controls data transfer for a disk drive, col. 4, lines 42-44 and col. 8, lines 30-64).

11. Regarding claim 14, Dunstan together with Hetzler taught the claimed information device therefore together they also teach the claimed method.

12. Regarding claim 17, Dunstan together with Hetzler taught the claimed information device and method therefore together they also teach the claimed recording medium that stores a program of the method.

Response to Arguments

13. Applicant's arguments filed 7/28/05 have been fully considered but they are not persuasive.

14. The applicants argue in substance that the instant application is different from Dunstan because it detects peak power from IO Packets. The examiner does not disagree that Dunstan does not use IO Packets that is why reference to Hetzler is relied upon to teach this feature. Dunstan teaches monitoring components and changing power modes of components during

peak-power (to prevent exceeding electrical current capabilities, col. 2, lines 1-9 and col. 2, lines 38-49).

15. Applicants argue in substance that the present invention is to immediately detect a termination of the peak-power generation and to simply and surely place from a current mode to the normal-mode, thereby minimizing the restriction which occur during the power-saving mode. It is noted that the features upon which applicant relies (i.e., immediately detect a termination of the peak-power generation and to simply and surely place from a current mode to the normal-mode, thereby minimizing the restriction which occur during the power-saving mode) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

16. Applicants argue in substance that it is possible to assume that Hetzler monitors access from signals between hardware modules and is thus different from the instant application that detects access information from the IO packets between the software modules. It is noted that the features upon which applicant relies (i.e., detects access information from the IO packets between the software modules) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

17. Applicants argue in substance that the processor in claim 4 is a main processor that causes the peak power. It is noted that the features upon which applicant relies (i.e., the processor in claim 4 is a main processor that causes the peak power) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations

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from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

Allowable Subject Matter

18. Claims 2-3, 6, 9, 11-12 and 15-16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

19. The following is a statement of reasons for the indication of allowable subject matter: see page 15, second paragraph regarding claims 2, 6, 9, 11 and 15. See third paragraph on page 15 regarding claims 3, 12 and 16.

Conclusion

20. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

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however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.


Any inquiry concerning this communication or earlier communications from the examiner should be directed to James K. Trujillo whose telephone number is (571) 272-3677.

The examiner can normally be reached on M-F (7:00 am - 4:30 pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (571) 272-3670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James Trujillo
October 5, 2005


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